



08/22/03

Case No. 62601

DIRECTOR,
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WASHINGTON, DC 20231

Transmitted herewith for filing is the patent
application of:

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DATE OF DEPOSIT: August 22, 2003

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22141 U.S. PTO
10/647060
08/22/03



Inventor: Robert J. MEARS

For: SEMICONDUCTOR DEVICE INCLUDING BAND-ENGINEERED SUPERLATTICE

☒ Patent Application: 34 pages, 71 claims.
☒ 9 sheets of drawings.
☒ The suggested drawing figure to be published is FIG. 1.
☒ Applicant qualifies as a small entity under 37 CFR § 1.27.
 Assignee info:
 Name: MEARS CORPORATION
 Address: 189 Wells Avenue, Suite 100, Newton, MA 02459
 State of Incorporation: Massachusetts

☒ Applicant claims priority benefit to the following U.S. applications:

Application No.: *****
 Filing Date: June 26, 2003

Application No.: *****
 Filing Date: June 26, 2003

☒ The Declaration and Filing Fee are NOT ENCLOSED.☒ Name, Address and Citizenship of Inventors are as follows:


Robert J. MEARS
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☒ PLEASE ADDRESS ALL CORRESPONDENCE TO ATTORNEY OF RECORD: CHRISTOPHER F. REGAN☒ Associate this file with Customer No. 27975.

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Date: August 22, 2003


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